

Digital Logic Homework Assignment

Draw the logic diagram and generate the truth table for the following expressions:
Use inverters, AND gates, and NOR gates as required. Label diagrams.

a. $F = W \bar{X} \bar{Y} + \bar{W} Z + Y Z$

b. $F = A \langle (B \bar{D} + \bar{B} D) \rangle + D \langle (BC + \bar{B} \bar{C}) \rangle$